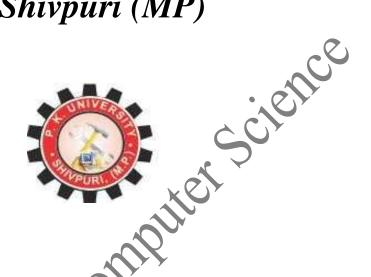
# Faculty of Computer Science & Application P.K.University Shivpuri (MP)



Evaluation Scheme & Syllabus

Bachelor of Science (Computer Science)

(I Year)

## Course Structure , Scheme of Examination & Syllabus Exam Scheme: Theory 70 Internal 30

#### **FIRST SEM**

Practical (Internal +External)=25+25=50

S.NO	Subject Code	Subject Name	Theory	Internal	practical	total
1	Computer Systems and&	BCS-101	70	30	50	150
	Programming in C		,			
2	Language &	BCS-102	70	30	-	100
	Communication					
3	Computer Organization	BCS-103	70	30	-	100
4	Discrete Mathematics	BCS-104	70	30	-	100
5	Principle of Management	BCS-105	70	30	-	100
		X				
	GRAND TOTAL	0,				550

#### **SECOND SEM**

S.NO	Subject Code	Subject Name	Theory	Internal	Practical	Total
1	Data Structure using 'C'	BCS-201	70	30	50	150
2	Organization Behavior	BCS-202	70	30	-	100
3	Computer Based Numerical & Statistical Techniques	BCS-203	70	30	50	150
4	Computer Networks	BCS-204	70	30	-	100
5	Digital Electronics	BCS-205	70	30	-	100
	GRAND TOTAL					600

### Paper Code & Title: BCS-101- Computer Systems & Programming in C

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Unit	Contents
I	<b>Unit1:</b> Introduction to digital computer, basic c operations of computer, functional components of
1	computer, Classification of computers.
	Introduction to operating system: [DOS, Windows, Linux and Android] purpose,
	function, services and types.
	Number system: Binary, octal and hexadecimal number systems, their mutual conversions,
	Binary arithmetic.
	Basics of programming: Approaches to Problem Solving, Concept of algorithm and
	flow charts, Types of computer languages:- Machine Language, Assembly language and High
TT	Level Language, Concept of Assembler, Compiler, Loader and Linker.
II	Standard I/O in C Standard I/O in C Fundamental data types- Character type,
	integer, short, long, unsigned, single and double floating point, Storage classes-
	automatic, register, static and external,
	Operators and expression using numeric and relational operators, mixed operands, type
	conversion, logical operators, bit operations, assignment operator, operator precedence and
	associativity. Fundamentals of C programming: Structure of C program, writing and executing
	the first C program, components of C language.
III	Arrays: Array notation and representation, manipulating array elements, using
	multidimensional arrays, arrays of unknown or varying size, Sequential search,
	Sorting arrays, Strings, Structures: Purpose and usage of structures, declaring
	structures, assigning of structures, Pointers to Objects: Pointer and address
	arithmetic, pointer operations and declarations, using pointers as function arguments,
	Dynamic memory allocation, Union, Enumeration, The Standard C Preprocessor:
<b>&gt;</b>	Defining macros and calling macros, utilizing conditional compilation, passing
	values to the compiler, The Standard C Library: Input/output: fopen, fread,
	etc, string handling functions, Math functions :log, sine, like Other Standard C
	functions.

Arrays: Array notation and representation, manipulating array elements, using multidimensional arrays. Structure, union, enumerated data types
<b>Pointers</b> : Introduction, declaration, applications File handling standard C preprocessors, defining and calling macros, conditional compilation, passing values to the compiler.

#### **Reference Books:**

1. Problem Solving and Program Design in C, by Jeri R. Hanly, Elliot B. Koffman, Pearson

Addison-Wesley, 2006.

- 2. Computer Science- A Structured Programming Approach Using C, by Behrouz A. Forouzan, Richard F. Gilberg, Thomson, Third Edition [India Edition],2007.
- 3.Computer System and Programming in C By Dr. AnandK, Tripathi, Dr. Monika Tripathi, Laxmi

Paper Code & Title: BCS-102-Language & Communication

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Unit	Contents	Contact
		Hours
I	Fundamentals of Communication Technical Communication: features	14
	Distinction between General and Technical communication; Language as a too	
	ofcommunication;Levelsofcommunication:Interpersonal,Organizational,	
	Mass communications; The flow of Communication: Downward, Upward	,
	Lateral of Horizontal (Peer group): Importance of technical communication	,
	Barriers to Communication.	
II	Constituents of Technical Written Communication Words and Phrases: Word	14
	formation. Synonyms and Antonyms; Homophones; Select vocabulary of about	
	500-1000 New words; Correct Usage: all Parts of Speech; Modals; Concord	
	Articles; Infinitives; Requisites of Sentence Construction: Paragraph	
	Development: Techniques and Methods-Inductive, Deductive, Spatial, Linear	
	Chronological etc; The Art of Condensation-various steps. Business	
	Communication Principles, Sales & Credit letters; Claim and Adjustmen	
	Letters; Job application and Resumes. Reports: Types; Significance; Structure	
	Style & Writing of Reports, Technical Proposal; Parts; Types; Writing o	1
	Proposal; Significance. Negotiation & Business Presentationskills.	
III	Presentation Strategies and Listening Skills. Defining Purpose; Audience &	14
	Local; Organizing Contents; Preparing Outline; Audio-visual Aids; Nuances o	
	Delivery;BodyLanguage;DimensionsofSpeech:Syllable;Accent;Pitch;	
	Rhythm; Intonation, Paralinguistic features of voice; Listening Skills: Active	
	Listening, Passive Listening. Methods for improving Listening Skills.	

#### Reference Books:

- 1. Communication Skills for Engineers and Scientists, Sangeeta Sharma et.al. PHI Learning Pvt.Ltd, 2011, NewDelhi.
- 2. Business Correspondence and Report Writing by Prof. R.C.Sharma & Krishna Mohan, Tata McGraw Hill & Co.Ltd.,2001, NewDelhi.

#### **Text Books:**

- 1. Improve Your Writing ed. V.N. Arora and Laxmi Chandra, Oxford Univ. Press, 2001, New Delhi
- 2. Technical Communication: A Practical Approach: Madhu Rani and Seema Verma- Acme Learning, NewDelhi-2011
- 3. Technical Communication- Principles and Practices by Meenakshi Raman & SangeetaSharma, Oxford Univ. Press,2007, NewDelhi.

Paper Code & Title: BCS-103:- Computer Organization

I Register Transfer Language, Bus and Memory Transfers, Bus Architecture, Bus Arbitration, Arithmetic Logic, Shift Micro operation, Arithmetic Logic Shift Unit, Design of Fast address, Arithmetic Algorithms (addition, subtraction, Booth Multiplication), IEEE standard for Floating point numbers. Hardwired & Micro Programmed (Control Unit): Fundamental Concepts (Register Transfers, Performing of arithmetic or logical operations, Fetching a word from memory, storing a word in memory), Execution of a complete instruction, Multiple-Bus organization.  II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Gontrol, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		L	T	J
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Arbitration, Arithmetic Logic, Shift Micro operation, Arithmetic Logic Shift Unit, Design of Fast address, Arithmetic Algorithms (addition, subtraction, Booth Multiplication), IEEE standard for Floating point numbers. Hardwired & Micro Programmed (Control Unit): Fundamental Concepts (Register Transfers, Performing of arithmetic or logical operations, Fetching a word from memory, storing a word in memory), Execution of a complete instruction, Multiple-Bus organization.  II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)			Hours	
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Booth Multiplication), IEEE standard for Floating point numbers. Hardwired & Micro Programmed (Control Unit): Fundamental Concepts (Register) Transfers, Performing of arithmetic or logical operations, Fetching a word from memory, storing a word in memory), Execution of a complete instruction, Multiple-Bus organization.  II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		Arbitration, Arithmetic Logic, Shift Micro operation, Arithmetic Logic Shift		
Micro Programmed (Control Unit): Fundamental Concepts (Register Transfers, Performing of arithmetic or logical operations, Fetching a word from memory, storing a word in memory), Execution of a complete instruction, Multiple-Bus organization.  II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts &Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		Unit, Design of Fast address, Arithmetic Algorithms (addition, subtraction,		
Performing of arithmetic or logical operations, Fetching a word from memory, storing a word in memory), Execution of a complete instruction, Multiple-Bus organization.  II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		Booth Multiplication), IEEE standard for Floating point numbers. Hardwired &		
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II Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		Performing of arithmetic or logical operations, Fetching a word from memory,		
<ul> <li>Hardwired Control, Micro programmed control (Microinstruction, Microprogram sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer &amp; manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts &amp;Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.</li> <li>Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)</li> </ul>		storing a word in memory), Execution of a complete instruction, Multiple-Bus		
program sequencing, Wide-Branch addressing, Microinstruction with Next address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts &Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1		organization.		
address field, Prefetching Microinstruction). Processor Organization: General register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1	II	Hardwired Control, Micro programmed control (Microinstruction, Micro-	14	
register organization, Stack organization, Addressing mode, Instruction format, Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM)		program sequencing, Wide-Branch addressing, Microinstruction with Next		
Data transfer & manipulations, Program Control, Reduced Instruction Set Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1		address field, Prefetching Microinstruction). Processor Organization: General		
Computer. I/O Interface, Modes of transfer, Interrupts &Interrupt handling, Direct Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1		register organization, Stack organization, Addressing mode, Instruction format,		
Memory access, Input-Output processor, SerialCommunication.  III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1		Data transfer & manipulations, Program Control, Reduced Instruction Set		
III Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM 1		Computer. I/O Interface, Modes of transfer, Interrupts & Interrupt handling, Direct		
		Memory access, Input-Output processor, SerialCommunication.		
Chips), organization of 2D and 21/2D. Auxiliary memory, Cache memory.	III	Memory Organization: Memory Hierarchy, Main Memory (RAM and ROM	14	
1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -		Chips), organization of 2D and 21/2D, Auxiliary memory, Cache memory,		
Virtual Memory, Memory management hardware.		Virtual Memory, Memory management hardware.		

#### **Reference Books:**

- 1. Computer Organization, Vravice, Zaky & Hamacher (TMHPublication).
- 2. Structured Computer Organization, Tannenbaum(PHI).
- 3. Computer Organization, Stallings (PHI).
- 4. Computer Organization, John P.Hayes (McGrawHill).

#### **Text Book:**

1. Computer System Architecture, M. Mano(PHI).

Paper Code & Title: BCS-104:-Discrete Mathematics

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Unit	Contents	Contact
		Hours
Ι	Set Theory: Definition of sets, countable and uncountable sets, Venn Diagrams,	14
	proofs of some general identities on sets. Relation: Definition, types of relation,	
	composition of relations, Pictorial representation of relation, equivalence	
	relation, partial ordering relation. Function: Definition, type of functions, one to	
	one, into and onto function, inverse function, composition of functions,	
	recursively defined functions.	
II	Mathematical Induction: Piano's axioms, Mathematical Induction D iscrete	14
	Numeric Functions and Generating functions Simple Recurrence relatio n with	
	constant coefficients, Linear recurrence relation without constant coeff icients.	
	Algebraic Structures: Properties, Semi group, Monoid, Group, Abelian group,	
	properties of group, Subgroup, Cyclic group, cosets, Permutation groups,	
	Homomorphism, Isomorphism and Automorphism ofgroups.	
III	Propositional Logic: Preposition, First order logic, Basic logical operations,	14
	Tautologies, Contradictions, Algebra of Proposition, Logical implication,	1.
	Logical equivalence, Normal forms, Inference Theory, Predicates and quantifiers,	
	Posets, Hasse Diagram and Lattices: Introduction, ordered set, Hasse diagram of	
	partially, ordered set, isomorphic ordered set, well ordered set, properties of	
	Lattices, and complemented lattices.	
	Lattices, and complemented attices.	

#### **Reference Books**

- 1. Chowchary, K. R. "Fundamentals of discrete Mathematical Structures",  $2^{Ed}$ , PHILearning.
- 2. Liptschutz, Seymour, "Discrete Mathematics", TMH.
- 3. Kenneth H. Rosen, "Discrete Mathematics and its applications", TMH.

#### **Text Books:**

1. Trembley, J.P & R. Manohar, "Discrete Mathematical Structure with Application to Computer Science", TMH.

#### I SEMESTER ( I YEAR )

Paper Code & Title: BCS-105:-Principle of Management

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Unit	Contents	Contact	
		Hours	
I	Evolution of Management: - Contribution of Taylor, Mayo & Fayol, Different		
	approaches of management, role of manager, tasks of a professional manager,		
	Management & its functions. Level of Management, managerial skills at various		
	levels. Planning & Decision making: - Definition, Nature for planning,		
	importance, Process of planning, decision making, nature importance & process,		
	types of plans.		
II	Organization & staffing: - Definition, organizing process, importance of	14	Ī
	organizing, Departmentation manpower planning, Recruitment, Selection,		
	Training & promotion. Directing & Leadership:- X Theory, & Y Theory,		
	Hawthorne & Tinstone studies Leadership. Definition, Stogdill trait theory,		
	Managerial grid, Fiedlers contingency approach		
III	Motivation - Meaning, Missions, Herzberg's theory, V Room's expectancy		1
	theory & Porter & Lawler model of Motivation. Communication & control		
	Communication Definition, importance, process, types, factors affecting		
	communication methods, barriers & remedies.		

- 1. Principles & Practice of Management L. M.Prasad 2. Management Theory & Practice C. B.Gupta

Paper Code & Title: BCS-201-Data Structure using 'C'

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Unit	Contents			Contact
				Hours
I	Introduction: Basic Terminology, Elementary Data Organization, Data S		e	14
	operations, Algorithm Complexity and Time-Space trade-off Arrays; Arra	,•		
	Definition, Representation and Analysis, Single and Multidimensional	_		
	address calculation, application of arrays, Character String in C, Character			
	operation, Array as Parameters, Ordered List, Sparse Matrices, and			
	Stacks: Array Representation and Implementation of stack, Operat			
	Stacks: Push & Pop, Array Representation of Stack, Linked Represent	ation o	of	
	Stack, Operations Associated with Stacks,			
	Application of stack: Conversion of Infix to Prefix and Postfix Expr			
	Evaluation of postfix expression using stack Recursion: Recursive de			
	and processes, recursion in C, example of recursion, Tower of Hanoi P			
	simulating recursion. Backtracking, recursive algorithms, principles of re	cursion	n,	
	tail recursion, removal of recursion.			
II	Queues: Array and linked representation and implementation of		S,	14
	Operations on Queue: Create, Add, Delete, Full and Empty. Circular queu			
	Dequeue, and Priority Queue, Linked list: Representation and Implement			
	Singly Linked Lists, Two-way Header List, Traversing and Searching of			
	List, Overflow and Underflow, Insertion and deletion to/from Linke			
	Insertion and deletion Algorithms, Doubly linked list, Linked List in			
	Polynomial representation and addition, Generalized linked list,	Jarbag	ge	
	Collection and Compaction.			
III	Trees: Basic terminology, Binary Trees, Binary tree representation, al		c	14
	Expressions, Complete Binary Tree. Extended Binary Trees, Array and Li			
	Representation of Binary trees, Traversing Binary trees, Threaded Bina			
	Traversing Threaded Binary trees, Huffman algorithm. Searching and I	•	_	
	Sequential search, binary search, comparison and analysis, Hash Table			
	Functions, Collision Resolution Strategies, Hash Table Implementation.			
	Insertion Sort, Bubble Sorting, Quick Sort, Two Way Merge Sort, He Sorting on Different Keys, Practical consideration for Internal Sorting.			
	Search Trees: Binary Search Tree (BST), Insertion and Deletion is		-	
	Complexity of Search Algorithm, Path Length, AVL Trees, B-trees.	п БЭ.	Ι,	
	Complexity of Scarch Algorithm, Fam Length, AVL 11ees, B-tiees.			

- 1. Y. Langsam, M. Augenstin and A. Tannenbaum, Data Structures using C and C++, Pearson Education Asia, 2nd Edition, 2002.
- 2. Ellis Horowitz, S. Sahni, D. Mehta Fundamentals of Data Structures in C++, Galgotia Book Source, NewDelhi.
- 3. S. Lipschutz, Data Structures Mc-Graw Hill International Editions, 1986.
- 4. Jean-Paul Tremblay, Paul. G. Soresan, An introduction to data structures with Applications, Tata Mc-Graw Hill International Editions, 2<sup>nd</sup> Edition1984.
- 5. A. Michael Berman, Data structures via C++, Oxford University Press, 2002.



## II SEMESTER ( I YEAR ) Paper Code & Title: BCS-202:-Organization Behavior

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Unit	Contents	Contact Hours
I	Organizational Behavior – What is O.B., Nature and Structure and Structure of O.S. approaches to O.B. behaviorists frame work, social learning frame work. Basic understanding of Individual behaviors:-personality – meaning, development, Freudian stage, Neo Freudian stage.	14
II	Perception-nature, Importance, meaning, learning & perception. Attitudes & satisfaction:- nature, dimensions of attitudes, meaning of job satisfaction. Sources & consequences of job satisfaction. Job stress – meaning, causes & effects. Group dynamics: - Nature of Groups, types- committee organization, its nature & functions. Informal Organization structure, Informal communication system	
III	Conflicts – Organizational conflicts, types of conflict, Strategies of interpersonal conflicts. Group decision making & control:- Nature and meaning of decision making, phases of decision making process, Meaning of Control, elements of control process.	

- 1. Business Organization and Management by BhushanY.K.
- 2. Business Organization by GuptaC.B
- 3. Organizational Behavior by L.M.Prasad

#### Paper Code & Title: BCS-203-Computer Based Numerical & Statistical Techniques

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Unit	Contents		Contact
			Hours
I	Floating point Arithmetic: Representation of floating point numbers, Opera	tions,	14
	Normalization, Pitfalls of floating point representation. Errors in num	erical	
	computation. Iterative Methods: Zeros of a single transcendental equation an	ıd	
	zeros of polynomial using Bisection Method, Iteration Method, Regula-	-Falsi	
	method, Newton Raphson method, Secant method, Rate of convergence		
	iterative methods. Finite differences and Interpolation. Finite Differences	ences,	
	Difference tables. Polynomial Interpolation: Newton's forward and back	kward	
	formula Central Difference Formulae: Gauss forward and backward for	mula,	
		ewton	
	Divided difference formula, Hermit's Interpolation for unequalintervals.		
II	Numerical Differentiation and Integration: Introduction, Num	erical	14
	Differentiation, Numerical Integration, Trapezoidal rule, Simpson's	rules,	
	Boole's Rule, Weddle's Rule Fuler-Maclaurin Formula. SimultaneousLinear	r	
	Equations: Solutions of system of Linear equations, Gauss Elimination	direct	
	method and pivoting, Ill Conditioned system of equations, Refinement	nt of	
	solution. Gauss Jacobi and Gauss Seidel iterative methods, Rate of Converg	gence.	
	Solution of differential equations: Picard's Method, Euler's Method, Tay	ylor's	
	Method, Runge-Kutta methods, Predictor-correctormethods		
III	Curve fitting, Approximations and Regression Analysis: Method of	least	14
	squares, fitting of straight lines, polynomials, exponential curves	etc.	
	Approximation of functions by Chebyshev polynomials. Linear, Non-linea	r and	
	Multiple regressions. Statistical methods: Sample distributions, Tes	st of	
	Significance: Chi-Square Test, t and Ftest.		

#### **Reference Books:**

- 1. Jain, Iyengar and Jain, "Numerical Methods for Scientific and EngineeringComputations", New AgeInt.
- 2. Gupta S.P. and Kapoor, V.K., Fundamentals of Applied statistics, Sultan Chand &Sons.
- 3. Gupta S.P. and Kapoor, V.K., Fundamentals of Mathematical Statistics, Sultan Chand &Sons.

#### **Text Books:**

- 1. Rajaraman V., "Computer Oriented Numerical Methods", PHI
- 2. Gerald & Wheatley, "Applied Numerical Analyses", AW

Paper Code & Title: BCS-204-Computer Networks

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Unit	Contents		Contact
		U	Hours
Ι	Basic Concepts: Components of data communication, distributed pro	cessing,	14
	standards and organizations. Line configuration, topology, Transmission		
	and categories of networks. OSI and TCP/IP Models: Layers and their fu		
	comparison of models. Digital Transmission: Interfaces and Modems		
	DCE Interface, Modems, Cable modems. Transmission Media: Guid		
	unguided, Attenuation, distortion, noise, throughput, propagation speed a	nd time,	
	wavelength, Shannon capacity, comparison of media.		
II	Telephony: Multiplexing, error detection and correction: Many to one,		14
	many, WDM, TDM, FDM, Circuit switching, packet switching and	_	
	switching. Data link control protocols: Linediscipline, flow control, error		
	synchronous and asynchronous protocols, character and bit oriented protocols.		
	Link access procedures. Point to point controls: Transmission states, PPI		
	LCP, Authentication, NCP. ISDN: Services, Historical outline, sub-	scriber's	
	access, ISDN Layers and broadcastISDN.		
III	Devices: Repeaters, bridges, gateways, routers, The Network		14
	Designissues, Routing algorithms, Congestion control Algorithms, Qu		
	service, Internetworking, Network-Layer in the internet. Transport an	1 1	
	layers in OSI Model. Transport layer functions, connection mana	igement,	
	functions of session layers, presentation layer and applicationlayer.		

- A.S.Tanenbaum, "Computer Networks"; Pearson Education Asia, 4<sup>th</sup> Ed.2003.
   Behrouz A. Forouzan, "Data Communication and Networking", 3<sup>rd</sup> Ed, Tata McGraw Hill, 2004.
- 3. William stallings, "Data and computer communications", Pearson education Asia, 7<sup>th</sup> Ed.,2002.

Paper Code & Title: BCS-205-Digital Electronics

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I Digital computers and Digital systems, Number systems: Binary number system, Octal & Hexadecimal number system, Conversion of Number System, Complements: r's and (r-1)'s complement, Signed Binary numbers, Binary codes, Arithmetic operations on Binary numbers. Logic Gates: AND, OR, NOT GATES and their Truth tables, NOR, NAND & XOR gates. Boolean Algebra: Law's, Postulates and theorems, Universal building blocks, logic diagrams,	Hours 14
system, Octal & Hexadecimal number system, Conversion of Number System, Complements: r's and (r-1)'s complement, Signed Binary numbers, Binary codes, Arithmetic operations on Binary numbers. Logic Gates: AND, OR, NOT GATES and their Truth tables, NOR, NAND & XOR gates. Boolean Algebra:	14
Complements: r's and (r-1)'s complement, Signed Binary numbers. Binary codes, Arithmetic operations on Binary numbers. Logic Gates: AND, OR, NOT GATES and their Truth tables, NOR, NAND & XOR gates. Boolean Algebra:	
Arithmetic operations on Binary numbers. Logic Gates: AND, OR, NOT GATES and their Truth tables, NOR, NAND & XOR gates. Boolean Algebra:	
GATES and their Truth tables, NOR, NAND & XOR gates. Boolean Algebra:	
, ,	
Law's, Postulates and theorems, Universal building blocks, logic diagrams,	
Converting circuits to universal logic, Minimization techniques: K -Map, Sum of	
Product & Product of Sum, Tabulationmethod.	
II Combinational circuits: Adders, Subtractors, Binary parallel adders,	14
Adder/Subtractor, Decimal adder, Code conversion, Magnitude com parator,	
Multiplexers, Demultiplexers, Decoders & Encoders.	
III Flip-flops: Types of Flip Flop: R-S, D, J-K, T, Master Slave, Triggering of flip-	14
flops, state reduction and assignment Conversion of flip-flops and State	
Realization of one Flip Flop Using Other Flip Flop. Registers and Counters:	
Shift Registers, Types of registers, Universal Shift Register with parallel load, Bi-	
directional Shift register, Ripple counters, synchronous counters, Ring counters,	
Johnson counter, Modcounters.	

- 1. Moris Mano, Digital Logic and Computer Design, Prentice Hal ofIndia.
- 2. Moris Mano, Digital Design, Prentice Hal ofIndia.
- 3. R.K. Gaur, Digital Electronics and Microcomputer, Dhanpat RaiPublication
- 4. R.P. Jain, Modern Digital Electronics, TataMcGraw-Hil
- 5. Malvino &Leach, Digital Principles and Aplications, TataMcGraw-Hil.
- 6. Rajaraman & Radhakrishanan, An introduction to Digital Computer Design, Prentice Hallof India.

Department of Computer Science

# Faculty of Computer Science & Application P.K.University Shivpuri (MP)



Evaluation Scheme & Syllabus
Bachelor of Science (Computer Science)
(II Year)

# Course Structure, Scheme of Examination & Syllabus Exam Scheme: Theory 70 Internal 30 Practical (Internal +External) =25+25=50

#### **THIRD SEM**

S.NO	Subject Code	Subject Name	Theory	Internal	Practical	Total
1	Object Oriented	BCS-301	70	30	50	150
	Programming Using C++		A	•		
2	Design and Analysis of	BCS-302	70	30	50	150
	Algorithms					
3	System Analysis & Design	BCS-303	70	30	-	100
			V			
4	Operating Systems	BCS-304	70	30	-	100
		$\wedge$ $\bigcirc$ $'$				
5	Financial Accounting &	BCS-305	70	30	-	100
	Management	X				
	GRAND TOTAL	O >				600
	×					

#### **FOURTH SEM**

S.NO	Subject Code	Subject Name	Theory	Internal	Practical	Total
1	.Net Framework using C#	BCS-401	70	30	50	150
2	Software Engineering	BCS-402	70	30	-	100
3	Business Communication	BCS-403	70	30	-	100
4	Optimization Technique	BCS-404	70	30	-	100
5	Database Management System	BCS-405	70	30	50	150
	GRAND TOTAL					600

#### Paper Code & Title: BS-301-Object Oriented Programming Using C++

L T P 0

T T 24	Continuts	<u> </u>
Unit	Contents	Contact
		Hours
I	Introduction: Introducing Object – Oriented Approach, Relating to other paradigms (Functional, Data decomposition). Basic concepts: Abstraction, Encapsulation, Inheritance, Polymorphism, Review of C, Difference between C and C++ cin, cout, new, delete, operators. Classes and Objects: Encapsulation, information hiding, abstract data types, Object & classes, attributes, methods, C++ class declaration, State identity and behavior of an object, Constructors and destructors, instantiation of objects, Default parameter value, object types++ garbage collection, dynamic memory allocation, Meta class / abstractclasses.	
II	Inheritance and Polymorphism: Inheritance, Class hierarchy, derivation – public, private & protected, Aggregation, composition vs classification hierarchies, Polymorphism, Categorization of polymorphism techniques, Method polymorphism, Polymorphism by parameter, Operator overloading, Parametric Polymorphism.	14
III	Generic function: Template function, function name overloading, Overriding inheritance methods, Run time polymorphism, Multiple Inheritance. Files and Exception Handling: Streams and files, Namespaces, Exception handling, Generic Classes.	14

- 1. A.R. Venugopat Rajkumar, T. Ravishanker "Mastering C++", TMH,1997.
- 2. S.B.Lippman & J.Lajoie, "C++ Primer", 3rd Edition, Addison Wesley, 2000. The C programming Lang., Person Ecl Dennis Ritchie
- 3. R.Lafore, "Object Oriented Programming using C++", Galgotia Publications, 2004.
- 4. E. Balagurusamy, "Object Oriented Programming with C++", TMH.
- 5. Herbert Sehlidt, "The Complete Reference c++", TMH.
- 6. Schaum's Outline, Programming with C++,TMH.

#### Paper Code & Title: BS-302- Design and Analysis of Algorithms

L T P 3 1 0

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Unit	Contents	Contact
		Hours
I	Introduction: Algorithms, Analysis of Algorithms, Design of Algorithms,	14
	Complexity of Algorithms, Asymptotic Notations, Growth of function,	
	Recurrences and their solution methods. Sorting in polynomial Time: Insertion	
	sort, Merge sort, Heap sort, and Quick sort Sorting in Linear Time: Counting	
	sort, Radix Sort, Bucket Sort, Medians and order statistics Advanced Data	
	Structure: Red Black Trees, Augmenting Data Structure, Binomial Heap, B-	
	Tree, Fibonacci Heap, and Data Structure for Disjoint Sets, All kinds of	
	Algorithms on these data structures, Dictionaries and priority Queues, mergeable	
	heaps, concatenablequeues.	
II	Advanced Design and Analysis Techniques: Dynamic programming, Greedy	
	Algorithm, Backtracking, Branch-and-Bound, Amortized Analysis Graph	
	Algorithms: Elementary Graph Algorithms, Breadth First Search, Depth First	
	Search, Minimum Spanning Tree, Kruskal's Algorithms, Prim's Algorithms,	
	Single Source Shortest Path, All pair Shortest Path, Maximum flow and	
	Traveling SalesmanProblem	
III	Randomized Algorithms, String Matching, NP-Hard and NP-Completeness,	14
	Approximation Algorithms, Sorting Network, Matrix Operations, Polynomials	
	and FFT, Number Theoretic Algorithms.	

- 1. ThomasHCormenLeiserson"IntroductiontoAlgorithms",PHILearningPrivate Limited, DelhiIndia.
- 2. SaraBaaseandAllenVanGelder,ComputerAlgoritms:"IntroductiontoDesignandAnalysis", PearsonEducation
- 3. Jon Kleinberg and Eva Tardos "Algorithm Design", PearsonEducation
- 4. Brassard Bratley "Fundamental of Algorithms", PHI Learning Private Limited, DelhiIndia.
- 5. M T Goodrich "Algorithms Design", JohnWiley
- 6. Aho, "Design and Analysis of Computer Algorithms", PearsonEducation.
- 7. Horowitz and Sahani ,"Fundamentals of Computer Algorithms", Galgotia Publications Pvt. Ltd Delhi, India.

Paper Code & Title: BCS-303- System Analysis & Design

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3	1	0

I Overview of System Analysis and design: System Development life Cycle. Concept and models: requirements determination, Logical design, physical Design, test, planning, implementation planning and performance evaluation, interviewing, presentation skills; group – based approaches JAD. Information requirement Analysis; process modeling with physical data flow diagrams, data modeling with logical entity relationship diagrams; Developing a Proposal; Feasibility study and cost estimation. System Design, Design of input and control, design of output and control. File design database design, process design, user interface design, prototyping, software construction, documentation.	ontact Iours 14
I Overview of System Analysis and design: System Development life Cycle. Concept and models: requirements determination, Logical design, physical Design, test, planning, implementation planning and performance evaluation, interviewing, presentation skills; group – based approaches JAD. Information requirement Analysis; process modeling with physical data flow diagrams, data modeling with logical entity relationship diagrams; Developing a Proposal; Feasibility study and cost estimation. System Design; Design of input and control, design of output and control. File design database design, process design, user interface design, prototyping, software construction, documentation.  II Application Development Methodologies and CASE tools, information engineering, structured system analysis and design object oriented methodologies for application development data modeling, process modeling, user interface design and prototyping, use of computer aidedsoftware	
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methodologies for application development data modeling, process modeling, user interface design and prototyping, use of computer aidedsoftware	14
user interface design and prototyping, use of computer aidedsoftware	
engineering (CASE) tools in the analysis design implementation of information	
chambering (crish) tools in the untripie, acoust imprementation of information	
systems.	
III Design and Implementation of OO platform, Object Oriented Analysis and	14
design through object modeling technique, object modeling, dynamic modeling	
and functional modeling, object oriented design and objectoriented	
programming system for implementation, object oriented data bases. System	
Implementation, Hardware Software selection, System testing, System Training,	
Software design, System maintenance.	

- 1. Haryszkiewycz, I.T, "Introduction of System Analysis and Design" PHI1989.
- 2. Raja Raman, V," Analysis and Design of Information System" PHI1991.

Paper Code & Title: BCS-304- Operating Systems

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Unit	Contents	Contact
		Hours
I	Introduction: Definition and types of operating systems, Batch Systems, multi	14
	programming, time-sharing parallel, distributed and real-time systems,	
	Operating system structure, Operating system components and services, System	
	calls, system programs, Virtual machines. Process Management: Process	
	concept, Process scheduling, Cooperating processes Threads, Interprocess	
	communication, CPU scheduling criteria, Scheduling algorithms, Multiple-	
	processor scheduling, Real-time scheduling and Algorithmevaluation.	
II	Process Synchronization and Deadlocks: The Critical-Section problem,	14
	synchronization hardware, Semaphores, Classical problems of synchronization,	
	Critical regions, Monitors, Deadlocks-System model, Characterization,	
	Deadlock prevention, Avoidance and Detection, Recovery from deadlock,	
	Combined approach to deadlock handling. Storage management: Memory	
	Management-Logical and Physical Address Space, Swapping, Contiguous	
	Allocation, Paging, Segmentation with paging in MULTICS and Intel 386,	
	Virtual Memory, Demand paging and its performance, Page replacement	
	algorithms, Allocation of frames, Thrashing, Page Size and other considerations,	
	Demand segmentation, Disk structure, Disk scheduling methods, Disk	
	management, Recovery Disk structure, disk scheduling methods, Disk	
	management, Swap-Space management, Disk reliability.	
III	Security & Case Study: Protection and Security-Goals of protection, Domain of	14
	protection, Access matrix, Implementation of access Matrix, Revocation of	
	Access Rights, language based protection, The Security problem,	
	Authentication, One Time passwords, Program threats, System threats, Threat	
	Monitoring, Encryption. Windows NT-Design principles, System components,	
	Environmental subsystems, File system, Networking and program interface,	
	Linux system-design principles, Kernel Modules, Process Management,	
	Scheduling, Memory management, File Systems, Input and Output, Interprocess	
	communication, Network structure, security	
L	· · · · · · · · · · · · · · · · · · ·	

- 1. Abraham Siberschatz and Peter Baer Galvin, "Operating System Concepts", Fifth Edition, Addision-Wesley
- 2. Milan Milankovic, "Operating Systems, Concepts and Design", McGraw-Hill.
- 3. Harvey M Deital, "Operating Systems", AddisonWesley
- 4. Richard Peterson, "Linux: The Complete Reference", OsborneMcGraw-Hill

#### Paper Code & Title: BCS-305- Financial Accounting & Management

L T P 3 1 0

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Unit	Contents	Contact
		Hours
I	Overview: Accounting concepts, conventions and principles; Accounting	14
	Equation, International Accounting principles and standards; Matching of Indian	1
	Accounting Standards with International Accounting Standards. Mechanics of	1
	Accounting: Double entry system of accounting, journalizing of transactions;	1
	preparation of final accounts, Trading Account, Manufacturing Accounts, Profit	i
	&Loss Account, Profit &Loss Appropriation account and Balance Sheet, Policies	ı
	related with depreciation, inventory and intangible assets like copyright,	i
	trademark, patents and goodwill.	ı
II	Analysis of financial statement: Ratio Analysis- solvency ratios, profitability	14
	ratios, activity ratios, liquidity ratios, market capitalization ratios; Common Size	i
	Statement; Comparative Balance Sheet and Trend Analysis of manufacturing,	i
	service & banking organizations.	İ
III	Funds Flow Statement: Meaning, Concept of Gross and Net Working Capital,	14
	Preparation of Schedule of Changes in Working Capital, Preparation of Funds	ı
	Flow Statement and its analysis. Cash Flow Statement: Various cash and non-	ı
	cash transactions, flow of cash, preparation of Cash Flow Statement and its	ı
	analysis.	İ

- 1. Narayanswami Financial Accounting: A Managerial Perspective (PHI, 2ndEdition).
- 2. Mukherjee Financial Accounting for Management (TMH, 1stEdition).
- 3. Ramchandran & Kakani Financial Accounting for Management (TMH, 2ndEdition).
- 4. Ghosh T P Accounting and Finance for Managers (Taxman, 1stEdition).
- 5. Ashish K. Bhattacharya- Essentials of Financial Accounting (PHI, NewDelhi)
- 6. Ghosh T.P- Financial Accounting for Managers (Taxman, 3rdEdition)
- 7. Maheshwari S.N & Maheshwari S K A text book of Accounting for Management (Vikas, 1st Edition)
- 8. Gupta Ambrish Financial Accounting for Management (Pearson Education, 2ndEdition)
- 9. Chowdhary Anil Fundamentals of Accounting and Financial Analysis (Pearson Education, 1st Edition).

Paper Code & Title: BS-401-.Net Framework using C#

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Unit	Contents	Contact Hours		
I	Introduction to .NET Technology, Introduction to VB.NET, Software	14		
	development and Visual Basic .NET, Visual Basic .NET and .NET frame.			
	Visual Basic fundamentals: The Visual Basic .NET Development Environment,			
	The element of VB.NET, VB.NET operators, Software design, Conditional			
	structure and control flow, Methods.			
II	Classes and Objects: Types, Structure and Enumeration, Classes, Interfaces,	14		
	Exception handling and Classes, Collections, Arrays and other Data Structure.			
III	Advance design concepts, Patterns, Roles and Relationships, Advanced Interface	14		
	Patterns: Adapters and Delegates and Events Data Processing and I/O. Writing			
	Software with Visual Basic .NET, Interfacing with the End User, Introduction to			
	ASP.NET and C#.NET and their features.			
	nce Books:			

- 1. Jeffrey R. Shapiro "The Complete Reference Visual Basic .NET" TMH (2002Edition).
- 2. Rox "Beginner and Professional Edition VB.NET" Tata McgrawHill.
- 3. Steven Holzner "Visual Basic NET Black Book" Wiley DreamtechPublication.
- 4. Alex Homer, Dave Sussman "Professional ASP.NET1.1" WileyDreamtech.
- 5. Bill Evzen, Bill Hollis "Professional VB.NET 2003" WileyDreamtech.
- 6. Tony Gaddis "Starting Out VB.NET PROG.2nd Edition" WileyDreamtech.
- 7. Chris Ullman, Kauffman "Beg. ASP.NET1.1 with VB.NET 2003" WileyDreamtech.

Paper Code & Title: BS-402- Software Engineering

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Unit	Contents	Contact
		Hours
I	Introduction: Introduction to Software Engineering, Software Components,	14
	Software Characteristics, Software Crisis, Software Engineering Processes,	
	Similarity and Differences from Conventional Engineering Processes, Software	
	Quality Attributes. Software Development Life Cycle (SDLC) Models: Water	
	Fall Model, Prototype Model, Spiral Model, Evolutionary Development Models, Iterative EnhancementModels.	
II	Software Requirement Specifications (SRS): Requirement Engineering Process:	14
	Elicitation, Analysis, Documentation, Review and Management of User Needs,	
	Feasibility Study, Information Modeling, Data Flow Diagrams, Entity	
	Relationship Diagrams, Decision Tables, SRS Document, IEEE Standards for	
	SRS. Software Design: Basic Concept of Software Design, Architectural Design,	
	Low Level Design: Modularization, Design Structure Charts, Pseudo Codes,	
	Flow Charts, Coupling and Cohesion Measures, Design Strategies:Function	
	Oriented Design, Object Oriented Design, Top-Down and Bottom-Up Design.	
III	Software Testing and Maintenance: Testing Objectives, Unit Testing, Integration	14
	Testing, Acceptance Testing, Regression Testing, Top-Down and Bottom-Up	
	Testing Strategies: Test Drivers and Test Stubs, Structural Testing (White Box	
	Testing), Functional Testing (Black Box Testing), Test Data Suit Preparation,	
	Alpha and Beta Testing. Need for Maintenance, Categories of Maintenance:	
	Preventive, Corrective and Perfective Maintenance, Cost of Maintenance,	
	Software Re-Engineering, Reverse Engineering. Software Project Management and Other Software Engineering methodologies: Software Configuration	
	Management Activities, Change Control Process, Software Version Control, An	
	Overview of CASE Tools. Estimation of Various Parameters such as Cost,	
	Efforts, Schedule/Duration, Constructive Cost Models (COCOMO), Resource	
	Allocation Models, Software Risk Analysis and Management.	
	7 modulon viodels, bottware resk 7 marysis and management.	

#### **Reference Books:**

- 1. Rajib Mall, Fundamentals of Software Engineering, PHIPublication.
- 2. Jibitesh Mishra and Ashok Mohanty, Software Engineering:Pearson

#### **Text Books:**

- 1. R. S. Pressman, Software Engineering: A Practitioners Approach, McGrawHill.
- 2. K. K. Aggarwal and Yogesh Singh, Software Engineering, New AgeInternational Publishers.

Paper Code & Title: BS-403-Business Communication

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Unit	Contents	Contact		
		Hours		
I	Meaning, Nature, Scope, Definition of Communication, Types of	14		
	Communication, Communication Barriers, Principles of Communication.			
	Written Communication – Types of Letter, Letter lay-out, Essentials of an			
	effective letter writing, Need and function of Businessletter.			
II	Oral Communication - Types of oral communication Barriers to oral			
	communication, speedy – Introduction & Characteristic of good speech. Mass			
	Communication – Nature & Scope of Mass Communication, function of mass			
	communication - Media of mass communication, Role of Mass - Media in			
	India.			
III	Report Writing - What is report, Importance of Reports, Types ofreports,	14		
	Characteristic of good report selecting suitable types of reports.			

- Business Communication Monopoly & Monipally.
   Commercial Correspondence Ghosh & Bhushan.

#### IV SEMESTER (II YEAR)

Paper Code & Title: BS-404- Optimization Technique

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Unit	Contents	Contact
		Hours
I	Central Problem of linear Programming various definitions included Statements	14
	of basic theorem and also their properties, simplex methods, primal and dual	
	simplex method, transport problem, tic-tac problem, and its solution.	
	Assignment problem and its solution. Graphical Method Formulation, Linear	
	ProgrammingProblem.	
II	Characteristics of queuing system, Classification of Queuing Model Single	14
	Channel Queuing Theory, Generalization of steady state M/M/1 queuing models	
	(Model-I, Model-II). Replacement of item that deteriorates replacement of items	
	that fail. Group replacement and individual replacement.	
III	Cost involved in inventory problem- single item deterministic model economics	14
	long size model without shortage and with shorter having production rate infinite	
	and finite. Introduction, solution of sequencing problem Johnson s algorithm for	
	n jobs through 2machines.	

- 1. L. R. Foulds, An Introduction Optimization Techniques Undergraduate Texts in Mathematics 1981
- 2. A.R., Balling, R., and J.D. Hedengren Optimization Methods for Engineering Design, Parkinson, Brigham Young University, 2013.
- 3. C. B. Gupta, Optimization Techniques in Operation Research Paperback
- 4. R. Panneerselvam, Operations Research Paperback, Publisher: Prentice-Hall of India Pvt. Ltd; 2nd editionedition.

#### IV SEMESTER (II YEAR)

Paper Code & Title: BS-405- Database Management System (DBMS)

L T P 3 1 0

Unit	Contents	Contact
		Hours
I	Introduction: Characteristics of database approach, data models, database users, database schema, DBMS architecture and data independence, DBMS structure. E-R Modeling: Entity types, Entity set, attribute and key, relationships, relation types, roles and structural constraints, weak entities, enhanced E-R and object modeling, Sub classes; Super classes, inheritance, specialization and generalization. EER and ER to relational mapping: Data base design using EER to relational language.	
II	File Organization: Indexed sequential access files; implementation using B & B++ trees, hashing, hashing functions, collision resolution, extendible hashing, dynamic hashing approach implementation and performance. Relational Data Model: Relational model concepts, relational constraints, relational algebra SQL: SQL queries, programming using SQL.	14
III	Database Normalization: Functional Dependencies, Normal form up to 3rd normal form. Concurrency Control Transaction processing, locking techniques and associated, database recovery, security and authorization. Recovery Techniques, Database Security.	14

- 1. Abraham Silberschatz, Henry Korth, S.Sudarshan, "Database Systems Concepts", 4th Edition, McGraw Pill, 1997.
- 2. Jim Melton, Alan Simon, "Understanding the new SQL: A complete Guide", Morgan Kaufmann Publishers,1993.
- 3. A.K.Majumdar, P. Bhattacharya, "Database Management Systems", TMH,1996.
- 4. Bipin Desai, "An Introduction to database systems", Galgotia Publications, 1991



# Faculty of Computer Science & Application P.K.University Shivpuri (MP)



**Evaluation Scheme & Syllabus for Bachelor of Science (Computer Science)** 

(III Year)

## Course Structure, Scheme of Examination & Syllabus Exam Scheme: Theory 70 Internal 30

**Practical (Internal +External) =25+25=50** 

#### FIFTH SEM

S.NO	Subject Code	Subject Name	Theory	Internal	Practical	Total
1	Java Programming	BCS-501	70	30	50	150
2	Artificial Intelligence	BCS-502	70.	30	-	100
3	Computer Graphics & Multimedia	BCS-503	70	30	-	100
4	Linux Environment	BCS-504	70	30	50	150
	GRAND TOTAL	O.				500

#### **SIX SEM**

S.NO	Subject Code	Subject Name	INTERNAL	EXTERNAL	TOTAL
1	PROJECT 6		300	300	600

Paper Code & Title: BCS-501-Java Programming

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Unit	Contents	(		Contact
		1		Hours
I	Internet: Internet, Connecting to Internet: Telephone, Cable		Satellite	14
	connection, Choosing an ISP, Introduction to Internet services, E-M	ail c	oncepts,	
	Sending and Receiving secure E-Mail, Voice and Video Conferen	ncing	g. Core	
	Java: Introduction, Operator, Data type, Variable, Arrays, Control	Sta	tements,	
	Methods & Classes, Inheritance, Package and Interface, Exception	n H	andling,	
	Multithread programming, I/O, Java Applet, String handling, Netwo	rkin	g, Event	
	handling, Introduction to AWT, AWT controls, Layout manage			
	Images, Graphics.		·	
II	Java Swing: Creating a Swing Applet and Application, Programming		using	14
	Panes, Pluggable Look and feel, Labels, Text fields, Buttons, Toggle	b	uttons,	
	Checkboxes, Radio Buttons, View ports, Scroll Panes, Scroll Bars, L.	ists,	Combo	
	box, Progress Bar, Menus and Toolbars, Layered Panes, Tabbed Pane	•	s, Split	
	Panes, Layouts, Windows, Dialog Boxes, Inner frame. JDBS: The co		ectivity	
	Model, JDBS/ODBS Bridge, (5) Java.sql package, connectivity to		remote	
	database, Navigating through multiple rows retrieved from adatabase			
III	Java Beans: Application Builder tools, The bean developer kit(BDK	(), J/	AR files,	14
	Introspection, Developing a simple bean, using Bound properties	, Th	e Java	
	Beans API, Session Beans, Entity Beans, Introduction to Enterprise	-		
	(EJB), Introduction to RMI (Remote Method Invocation): A simple			
	application using RMI. Java Servlets: Servlet basics, Servlet API			
	cycle of a Servlet, Running Servlet, Debugging Servlets, Thread-s			
	HTTP Redirects, Cookies, Introduction to Java Server pages (JSP).		,	

- 1. Margaret Levine Young, "The Complete Reference Internet", TMH Education Pvt.Ltd.
- 2. Thampi, "Object Oriented Programming in JAVA" Wiley DreamtechPublication.
- 3. Balagurusamy E, "Programming in JAVA", Tata Mcgraw-hill Education Pvt.Ltd.
- 4. Dustin R. Callway, "Inside Servlets", Addison Wesley.
- 5. Mark Wutica, "Java Enterprise Edition", QUE.
- 6. Steven Holzner, "Java2 Black book", Wiley DreamtechPublication.
- 7. Liang, "Introduction to Java Programming, Comprehensive Version", PearsonEducation.
- 8. Deitel and Deitel, "Java: How to Program" PHI Learning Private Limited, DelhiIndia

Paper Code & Title: BCS-502-Artificial Intelligence

	3 $1$	0
Unit	Contents	Contact
		Hours
I	Introduction: Introduction to Artificial Intelligence, Foundations and History of	14
	Artificial Intelligence, Applications of Artificial Intelligence, Intelligent Agents,	
	Structure of Intelligent Agents. Computer vision, Natural Language Possessing.	
	Introduction to Search: Searching for solutions, Uniformed search strategies,	
	Informed search strategies, Local search algorithms and optimistic problems,	
	Adversarial Search, Search for games, Alpha - Beta pruning.	
II	Knowledge Representation & Reasoning Propositional logic, Theory of first	14
	order logic, Inference in First order logic, Forward & Backward chaining,	
	Resolution, Probabilistic reasoning, Utility theory, Hidden Markov Models	
	(HMM), Bayesian Networks. Machine Learning: Supervised and unsupervised	
	learning, Decision trees, Statistical learning models, Learning with complete	
	data - Naive Bayes models, Learning with hidden data –EM algorithm,	
	Reinforcement learning.	
III	Pattern Recognition. Introduction, Design principles of pattern recognition	14
	system, Statistical Pattern recognition, Parameter estimation methods - Principle	
	Component Analysis (PCA) and Linear Discriminant Analysis (LDA),	
	Classification Techniques - Nearest Neighbor (NN) Rule, Bayes Classifier,	
	Support Vector Machine (SVM), K – means clustering.	

- 1. Stuart Russell, Peter Norvig, "Artificial Intelligence A Modern Approach", Pearson Education.
- 2. Elaine Rich and Kevin Knight, "Artificial Intelligence", TMH Education Pvt.Ltd.
- 3. E. Charniak and D McDermott, "Introduction to Artificial Intelligence", PearsonEducation
- 4. Dan W. Patterson, "Artificial Intelligence and Expert Systems", Prentice Hall ofIndia

Paper Code & Title: BCS-503- Computer Graphics & Multimedia

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Unit	Contents	?	Contact Hours
I	Computer Graphics: definition, classification & Applications, Development Hardware & Software for Computer Graphics. Display devices, Hard condevices. Interactive Input devices, display processor, Line drawing; varied algorithms and their comparison, circle generation- Bresenham's mid-point circle drawing algorithm, mid-point ellipse drawing algorithm. Attributes output primitives, line style, color and intensity, Area filling algorithms, Soline algorithm, boundary fill flood fill algorithm, Ant aliasing techniques. To dimensional transformations; translation, scaling, rotation, reflection sheering composite transformation, transformation commands, charactergeneration.	ous of can wo ng,	14
II	Viewing coordinates, Window, view port, clipping, Window to view p transformation, line clipping algorithm; Cohen Sutherland, polygon clipping Sutherland hodgman algorithm, 3D clipping, normalized view volumes, vi port clipping, clipping in homogeneous coordinates. Illumination model:Light sources, diffuse reflection specular reflection, reflected light, intensity level surface shading; phong shading ground shading, color models like RGB, YI CMY, HSV etc.	ng; ew els,	14
III	3-D Viewing: Three-dimensional concepts, 3D display techniques, representation polygon & curved surfaces. Design of curves & surfaces- Bezie Method, B-Spline methods, 3D transformation transition, scaling, compost transformation rotation about arbitrary axis, projections: Parallel & Perspecti Hidden surface and line removal; back face removal, depth buffer and scan l methods. Introduction to multimedia, multimedia components, multime hardware, SCSI, IDE, MCI, Multimedia data and file formats, RTF, TIFF, MII JPEG, DIB, MPEG, Multimedia tools, presentations tools, Authoring tools, presentations.	er's site ve, ine dia	14

- 1. D.Hearn and M.P. Baker "Computer Graphics" (2nd ed), PHI.
- 2. S. Harrington "Computer Graphics a Programming approach" (2nd ed)McGrawhill.
- 3. New Mann & Sprovl- "Principles of interactive computer graphics" (2nd ed)McGrawhill.
- 4. Roger S. David "Procedural Elements for Computer Graphics", McGrawHill.
- 5. Roger S David "Mathematical Elements for Computer Graphics", McGrawHill.
- 6. Foley & Vandan "Computer Graphics Principles & Practice in "C" "AddisionWesly.
- 7. Tay Vaugham "Multimedia Making it Work" 5th Ed. 2001, Tata McGrawHill.
- 8. Prabhat K. Andleigh & Kiran Thakur "Multimedia System Design", PHI
- 9. Drew, "Fundamentals of Multimedia", Pearsons.
- 10. Nigel Chapman, J. Chapman "Digital Multimedia" WileyIndia.

Paper Code & Title: BCS-504- Linux Environment

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Unit	Contents	Contact
Unit	Contents	Hours
I	Overview of Linux: What is Linux, Linux's root in Unix, Common Linux	14
	Features, advantage of Linux, Overview of Unix and Linux architectures, Linux	
	files system, hardware requirements for Linux, Linux Internals: Introduction,	
	Process Management, System Calls.	
	Linux File system: Logging in, getting familiar with Linux desktop, shell	
	interface, understanding Linux Shell, Types of Text Editors, using vi editor,	
	prompt character, correcting typing errors, simple shell commands-date, cal,	
	who, tty, uname, password, BS, script, echo, logging out, Environment variables,	
	wild card characters, *, ?, absolute and relative path, listing files and directories	
	commands, navigating file system- pwd, cd mkdir, rmdir, ls, pr, Handling	
	ordinary files- cat, cp, mv, wc, rm, comm., amp, diff, Basic files attributes - file	
	permissions, changing gpermissions.	
II	Processes and filters: Simple filters- head, tail, cut, paste, sort, uniq, tr, Regular	14
	expression Grep utility, Shell command line, redirection, pipeline, spiltoutput,	
	tee, and process- System Processes, internal and external commands, background	
	process, premature termination of process, process priorities, processsch eduling	
	– (at, batch), nohup command.	
	Shell Programming: Interactive scripts, Shell variables, assigning values to	
	variables, positional parameters, command line arguments, arithmetic inshell	
	script, exit status of a command, sleep and wait, script termination.	
III	Decision taking- if else, nested if, file tests, string tests, case control structure.	14
	Loop control structure- while, for, IFS, break, continue, \$* and \$@, logical	
	operators & and executing script, Debugging a script, Debugging a script,	
	executing multiple scripts System Administration : Configuration of Linux,	
	Installation of Linux, Connecting to remote machines- ftp, telnet, Adding and	
_	removing users.	
L		

- 1. Kathleen Donovan, Linex (Red Hat) Introduction Spiral-bound 2000, Element K Press; Instructor's Edition edition(2000).
- 2. James K. L. Linux: Learning The Essentials, Phi Learning Pvt. Ltd.2012.
- 3. David Brickner O'Reilly Media Test Driving Linux: ,2005