		<u> </u>	PART A: Introduct	<u>ion</u>		
Program	m: UG Level .	Class: I Year		Year:	Session:	
	1	Subject:	Foundation Course	e (English)		
1.	Course Code		UENGLCA101			
2.	Course Title		English Language	and Indian Cultu	re	
3.	Course Type (Core Course/Elective/Ge Elective/ Vocation	eneric Ial	Foundation Cours			
4.	Pre-Requisite (if a	ny)	To study this cour of English langua students of UG lev	ge. This course	will be studied	by all the
5.	Course Learning O (CLO)	utcomes	Through this cours 1. Prepare for varie English language of 2. Promote their of variety of texts and 3. Build and enhat 4. Develop their grammar and usage 5. Inculcate value heritage and envir citizens.	ous competitive ecompetence. comprehension sk their interpretatence their vocal communication es. ues which make	xams by developing exposions.  bulary.  skills by street them aware of	sed to a engthening f national
6.	Credit Value		4 Credit			
7.	Total Marks		Max. Marks: 40+6	0	Min. Pass Marks:	16+24
		PART	13: Content of the	Course		6.
Total N	No. of Lectures-Tuto					
			Total No. of Lectures	S:		
Unit			Topics			No. of Lecture
I	<ul><li>2. National Education</li><li>3. The Axe- R.K N</li><li>4. The Wonder That</li></ul>	is Without Fear- on — M. K. Ga arayan [Key <b>W</b> t Was India- A.	ion Skills: — Rabindranath Tag ndhi [Key Word: E ord: Environment L Basham (an excen ajagopalachari [Key	dification] [] [rpt] [Key Word:	: Indianness]	05
II	Comprehension SI	xill:	ole choice questions			05
III_	Homophones, Homo	onyms and One-	Building: Suffix, Pre- word substitution. Adjective, Verb, Adjective, Verb, Adjective		•	05

# **PART C: Learning Resources**

Textbooks, Reference Books, Other Resources

## **Suggested Readings**

Essential English Grammar - Raymond Murphy, Cambridge University Press.

- Practical English Grammar Exercises 1- A. J. Thomson & A. V. Martinet, Oxford India.
- Practical English Usage Michael Swan, Oxford
  English Grammar in Use Raymond Murphy, Cambridge University Press.

	PART D: Asses	sment and Evaluation	
Internal Assessment : Continuous Comprehensive Evaluation (CCE) : 40 Marks Shall be based on allotted assignments and Class Tests. The marks shall be as follows:		External Assessment: University Exam (UE): 60 Marks Time: 03.00 Hours	
Assessment and presentation of assignment	10 Marks	Section (A): Five Very Short Questions (50 Words Each)	$05 \times 02 = 10 \text{ Marks}$ OR
Class Test I ( Objective Questions)	10 Marks	OR MCQ Questions	10 x 01 = 10 Marks
Class Test II (Descriptive Questions)	10 Marks	Section (B): Five Short Questions (200 Words Each)	05 x 06 = 30 Marks
Class Test III (Based on OS commands)	10 Marks	Section (C): Two Long Questions (500 Words Each)	$02 \times 10 = 20 \text{ Marks}$
Total	40 Marks	Total	60 Marks
Any remarks/suggestions:	0	Will Co	

Program:	PART A:. Introduction			
Certificate	Class: B.C.A. Year: 1 Yea	r Session:		
1	Course Code	UCOMPCA102		
2	Course Title	Computer Fundamentals, Organization and Architecture		
3	Course Type (Core Course/Elective/Generic Elective/ Vocational	Major — Paper I		
4	Pre-Requisite (if any)	To study this course, a student must have basic knowledge of Computer		
5.	Course Learning Outcomes (CIO)	After the completion of this course, a successful student will be able to:  1. Understand the basic structure, operation and characteristics of digital computer.  2. Design simple combinational digital circuits based on given parameters.  3. Understand the working of arithmetic and logic unit.  4. Know about hierarchical memory system including cache memories and virtual memory,  5. Know the contributions of Indians in the field of computer architecture and related technologies.		

Module Topics	No of Lectures
Fundamentals of computers: and limitations. Definition, Characteristics, capabilities  Types of Computers: Analog, Digital, Micro, Mini, Mainframe & Super computers. Generations of Computers Computers, Work Station. Server and applications. GIS, GPS, Cloud Computing and various public domains and Smart Systems: definition, characteristics Definition of Embedded system, Uses of computers in e-governance services.	NO OT LECTURES

II	Block diagram of computer and its functional units. Concept of hardware, software and firmware. Types of software.  Input devices - keyboard, scanner, mouse, light pen, bar code reader, OMR, OCR. MICR, track hail, joystick, touch screen camera, ink etc.  Output devices: monitors classification of monitors based on technology - CRT flat panel, LCD, LED monitors, speakers, printers — dot matrix printer, ink jet printer, laser printer, 3D Printers, Wi-Fi enabled printers, plotters , LCD/LED	10
III	Computer memory and its types, Storage devices: Magnetic tapes, Floppy Disks, Hard Disks, Compact Disc - CD-ROM, CD-RW, VCD, DVD, DVD-RW, USB drives, Blue Ray Disc, SD/MMC Mernot cards.  Fundamentals of Digital Electronics: Data Types, Complements, Fixed-Point Representation, Floating-Point Representation, Binary and other Codes. Error Detection Codes  Logic Gates, Boolean_ Algebra, Map Simplification, Combinational Circuits, Sequential Circuits, simple combinational circuit design problems.  Combinational Circuits- Adder-Subtractor, Multiplexer, Demultiplexer, Decoders, Encoders	15

IV Sequential Circuits - Flip Flops, Registers, Counters. IV Basic Computer Organization: Instruction codes, Computer Registers, Computer Instructions, Timing & Control, Instruction Cycles, Memory Reference Instruction, Input - Output & Interrupts Instruction formats, Addressing modes, Instruction codes, Machine language, Assembly language.

**Register Transfer and Micro operations:** Register Transfer Language, Register Transfer, Bus & Memory Transfer, Arithmetic Micro-operations,

Processor and Control Unit: Hardwired vs. Micro programmed Control Unit, General Register Organization, Stack Organization, Instruction Format, Data Transfer & Manipulation, Program Control, Introductory concept of RISC, CISC, advantages and disadvantages of both:

**Pipelining -** concept of pipelining, introduction to Pipelined data path and control

Data hazards & Control hazards. I/O Systems - Peripheral Devices, I/O Interface,

Data Transfer Schemes - Program Control, Interrupt, DMA Transfer. I/O Processor.

Memory Hierarchy, Processor vs. Memory Speed, High-Speed Memories, Main memory & its types, Auxiliary memory, Cache Memory, Associative Memory, Interleaving, concept of Virtual Memory, Hardware for Memory Management.

Indian contribution to the field - Contributions of reputed scientists of Indian origin - like - Dr. Vinod Dham - Father of Intel Pentium Processor, Dr, Ajay Ghat Co-Inventor of USB Technology, Dr, Vinod Khosla- co-founder of Sun Microsystems, Dr. Vijay P Bhatkar - architect of India's national initiative in supercomputing, and many others.

Parallel Computing projects of India PARAM, ANUPAM, FLOSOLVER, CHOPS etc. Other relevant contributors and contributions

# PART C Suggested study material

https://nptel.ac.in/

http://www.mphindigranthacademv.org/

	PART D: Asses	sment and Evaluation				
Internal Assessment : Continuous Comprehensive Evaluation (CCE) : 40 Marks Shall be based on allotted assignments and Class Tests. The marks shall be as follows:		External Assessment: University Exam (UE): 60 Marks Time: 03.00 Hours				
Assessment and presentation of assignment	10 Marks	Section (A): Five Very Short Questions (50 Words Each)	$05 \times 02 = 10 \text{ Marks}$ OR			
Class Test I (Objective Questions)	10 Marks	OR MCQ Questions	10 x 01 = 10 Marks			
Class Test II (Descriptive Questions)	10 Marks	Section (B): Five Short Questions (200 Words Each)	05 x 06 = 30 Marks			
Class Test III (Based on OS commands)	10 Marks	Section (C): Two Long Questions (500 Words Each)	02 x 10 = 20 Marks			
Total	40 Marks	Total	60 Marks			
Any remarks/suggestions:	Any remarks/suggestions:					

		PART A: Introduction	
Progra	m: Certificate Class: E		ession:
1.	Course Code	UOPERCA103	
2.	Course Title	Operating System	
3.	Course Type (Core Course/Elective/Generic Elective/ Vocational	Minor	
4.	Pre-Requisite (if any)	Open for all	
5.	Course Learning Outcomes (CLO)	<ul> <li>After the completion of this course, a be able to do the following:</li> <li>Describe the importance of computer s and the role of operating system in their policies and algorithms.</li> <li>Specify objectives of modern operating describe how operating systems have entime.</li> <li>Understand various process management can compare various scheduling techn synchronization, and deadlocks.</li> <li>Describe the concepts of memory management techniques.</li> <li>Identify the best suited process management for any process.</li> <li>Describe various file operations, file allowed and disk space management.</li> <li>To understand and identify potentiate operating systems and the security feature against them.</li> <li>Learn to operate the Linux system,</li> </ul>	system resources in management systems and evolved over ent concepts and niques, gement ement technique ocation I threats to guard
6.	Credit Value	Theory 4 Credits	
7.	Total Marks	Max. Marks: 40+60 Min. Passing Marks:	16+24
	PA	RT B: Content of the Course	
	No. of Lecture	es (in hours per week): 2 Hours per week	
	T	Cotal No. of Lectures: 60 Hrs.	
I I	Introduction to Operating S Evolution of OS, Basic OS Operating Systems— Bat Multiprocessing Systems, Tin systems. Operating System for Person Devices. Applications of various operations	stems — Windows, UNIX/Linux, Android,	No. of Lectures 6
II	Process Management: Process Block.	ss Concepts, Process states & Process Control heduling Criteria, Scheduling Algorithms	14

	MET D DIT MET TO DESCRIPT	
	Multiple-Processor, Real-Time, Multilevel Queue and Multilevel Feedback	
	Queue Scheduling.  Deadlack Definition Deadlack Characterization Necessary and Sufficient	
	<b>Deadlock -</b> Definition, Deadlock Characterization, Necessary and Sufficient Conditions for Deadlock.	
	Deadlock Handling Approaches: Prevention, Avoidance, Detection and	
	Recovery.	
III	-	1.4
111	Memory Management: Introduction, Address Binding, Logical versus Physical Address Space, Swapping, Contiguous & Non-Contiguous	14
	Allocation, Fragmentation (Internal & External), Compaction, Paging,	
	Segmentation, Virtual Memory, Demand Paging, Performance of Demand	
	Paging, Page Replacement Algorithms.	
	File Management: Concept of File System(File Attributes, Operations,	
	Types), Functions of File System, Types of File System, Access Methods	
	(Sequential, Direct & other methods), Directory Structure (Single-Level,	
	Two-Level, Tree-Structured, Acyclic-Graph, General Graph), Allocation	
	Methods (Contiguous, Linked, Indexed)	
IV	Disk Management: Structure, Disk Scheduling Algorithms (FCFS, SSTF,	12
1 4	SCAN, C-SCAN, LOOK), Swap Space Management, Disk Reliability,	12
	Recovery.	
	Security: Security Threats, Security policy mechanism, Protection, Trusted	
	Systems, Authentication and Internal Access Authorization, Windows	
	Security.	
V	LINUX: Introduction, History and features of Linux, advantages, hardware	12
	requirements for installation, Linux architecture, file system of Linux - boot	
	block, super block, mode table, data blocks.	
	Linux standard directories, Linux kernel, Partitioning the hard drive for	
	Linux, installing the Linux system, system - startup and shut-down process,	
	init and run levels. Process, Swap, Partition, fdisk, checking disk free spaces.	
	Difference between CLI OS & GUI OS, Windows v/s Linux, Importance of	
	Linux Kernel, Files and Directories. Concept of Open Source Software.	
VI	Indian contribution to the field — the BOSS operating system, open source	2
	softwares, growth of LINUX, Aryabhatt Linux, contributions of innovators —	
	RajenSheth, Sunder Pichai etc.	
	PART C: Learning Resources	
	Textbooks, Reference Books, Other Resources	
Suggeste	d Readings	
Textbool	ks:	
• A Sil	lberschatz, P.B. Galvin, G. Gagne, Operating Systems Concepts, 8th Edition,	
Publ	ications.	
• A.S.	. Tanenbaum, Modern Operating Systems, 3rd Edition, Pearson Education.	
	rating System by Peterson	
• Lini	ux by Sumitabha Das	
0 0	. 0 * , .	
Reference		
	Nutt, Operating Systems: A Modern Perspective, 2nd Edition Pearson Education.	
	Stallings, Operating Systems, Internals & Design Principles, 8th Edition, Pearson	
	Milenkovic, Operating Systems- Concepts and design, Tata McGraw Hill.	
<ul> <li>Ope</li> </ul>	rating System design and Concepts by Milan Milenkovic.	

	PART D: Assess	ment and Evaluation		
Internal Assessment : Continuous		External Assessment: University Exam (UE): 60		
Comprehensive Evaluation (C	,	Marks		
Shall be based on allotted assign Tests. The marks shall be as for		Time: 03.00 Hours		
Assessment and presentation	10 Marks	Section (A): Five Very	$05 \times 02 = 10 \text{ Marks}$	
of assignment		Short Questions (50 Words Each )	OR	
Class Test I (Objective Questions)	10 Marks	OR MCQ Questions	10 x 01 = 10 Marks	
Class Test II (Descriptive Questions)	10 Marks	Section (B): Five Short Questions (200 Words Each)	05 x 06 = 30 Marks	
Class Test III (Based on OS commands)	10 Marks	Section (C): Two Long Questions (500 Words Each)	02 x 10 = 20 Marks	
Total	40 Marks	Total	60 Marks	
Any remarks/suggestions:				



rogram:	Certificate Class	: B.C.A. Year: I Year	
	9	ubject: Computer Application	Session:
	Course Code	UORGACA104	
	Course Title	Organization Behaviour	
С;	Course Type (Core Course/Elective/Generic Elective/ Vocational	Generic Elective	
4.	Pre-Requisite (if any)	Not required	
5.	Course Learning Outcomes (CLO)	<ul> <li>After the completion of this course, a stude able to do the following:</li> <li>1. Understand the effect of interpersonal longanizational work-life</li> <li>2. Understand perspective in diverse cultures.</li> <li>3. Understand the principles of organ behavior with relevance to the Indian but</li> </ul>	oehavior in an ral environment izational huma
6.	Credit Value	Theory 6 Credits	
7.	Total Marks	Max. Marks: <b>40+60</b> Min. Passing Marks:	16+24
	PAF	RT B: Content of the Course	
	No. of Lecture	es (in hours per week): 3 Lectures per week	
	Marine Commence	Total No. of Lectures: 90 Hrs.	representative description of the
Module		Topics	No. of Lecture
I	Disciplines to Organizational opportunities in the field of		10
II		sonality, Perception, Social Perception and itude-characteristics, components, formation and ning and Re-enforcement.	15
III	Leader. Group Dynamics-	Theories of Leadership, Qualities of a good group formation, Nature of groups, Types of sources, Reasons of joining groups, Functions of	20
IV	•	ing, Causes, Effects and coping strategies for ts and theories of motivation.	10
V	Organizational change, confl	ict and peer: Forces of change, planned change, flict management and negotiation techniques.	25
VI	International Dimensions of Opportunities, Organizati Learning Organization. Case Studies.	of Organizational Behavior, Equal Employment onal Culture, Managing Cultural Diversity,	10
		PART C: Learning Resources	
<u> </u>		ks, Reference Books, Other Resources	
Suggeste	ed Readings		

Robbins S. P., Organizational Behaviour, 7th Ed., New Delhi, PHI, 1996

- Huse, F E and Cunnings T G, Organization Development and Change, 3<sup>rd</sup> ed., New York. West, 1985
- Shekcharam Uma, Organizational Behaviour, Text & cases, New Delhi THM, 1989.
- Singh Dalip, Emotional Intelligence at work, Response Books, Sage Publication, Delhi 2001.
- Book published by M.P. Granth Academy, Bhopal

#### **Reference Books:**

- Luthans Fred., "Organizational Behaviour", McGraw Hill.
- Hellriegel, Slocum and Woodman, Organizational Behavior, South-Western, Thomson Learning, 9th edition, 200 I.
- Behavior in Organizations, Jerald Greenberg, 8ih ed, Pearson Education.
- Arnold, John, Robertson, Ivan t. and Cooper, Cary, I., "Work psychology: understanding human behavior in the workplace", Macmillan India Ltd., Delhi.
- Dwivedi, R. S., "Human relations and Organizational Behaviour: a global perspective", Macmillan India Ltd., Delhi.

## Suggestive digital platform web links

- https://www.coursera.org/courses?query=economics
- https://www.mooc-list.com/tags/economics
- <a href="https://www.coursera.org/learn">https://www.coursera.org/learn</a>
- https://ocw.mit.edu/courses
- https://onlinecourses.nptel.ac.in/noc23\_ec06/preview
- https://archive.nptel.ac.in/courses/110/101/110101149/
- http://www.mphindigranthacademy.org/

Internal Assessment : Contin		sment and Evaluation  External Assessment: Univ	versity Exam (UE): 60
Comprehensive Evaluation (CCE): 40 Marks Shall be based on allotted assignments and Class Tests. The marks shall be as follows:		Marks Time: 03.00 Hours	
Assessment and presentation of assignment	10 Marks	Section (A): Five Very Short Questions (50 Words Each)	05 x 02 = 10 Marks OR
Class Test I (Objective Questions)	10 Marks	OR MCQ Questions	$10 \times 01 = 10 \text{ Marks}$
Class Test II (Descriptive Questions)	10 Marks	Section (B): Five Short Questions (200 Words Each)	05 x 06 = 30 Marks
Class Test III (Based on OS commands)	10 Marks	Section (C): Two Long Questions (500 Words Each)	02 x 10 = 20 Marks
Total	40 Marks	Total	60 Marks

		PART A: Introduction		
	Program: Certificate	Class: B.C.A Year: I Year Ser	ssion:	
1.	Course Code	UCOMPCA105		
2.	Course Title	Computer Fundamentals Organization An Lab	d Architecture	
3.	Course Type (Core Course/Elective/Generic Elective/ Vocational	Major — Paper I	_	
	Pre-Requisite (if any)	Open for All		
4.	Course Learning Outcomes(CLO)	After the completion of this course, a succe will be able to do the following:  1. Familiarity with parts of the computer devices used with the computer.  2. Realization of the basic logic and up 3. Verify the behavior of logic gates use 4. Implement Binary-to -Gray, Gray-conversions.	and peripheral niversal gates. sing truth tables.	
	6.	<ul><li>5. Design half and full adder circuit us</li><li>6. Design and construct flip flops and verifitables.</li></ul>		
	Credit Value	Practical - 2 Credits		
	Total Marks	Max.Marks: 40+60 Min. Passing Mark	s: 16+24	
	PA	RT B: Content of the Course		
		ticals (in hours per week): 1 Hrs. per week		
		Total No. of Labs: 30 Hrs.		
	I. Computer Fundame	gestive list of Practicals	No. of Labs.	
	b) Identify various par ports, buses, IC chr c) Identify various Iii II. Digital Electronics  a) Verification and in gates b) Verification and in gates	interpretation of truth table for AND, OR, NOT interpretation of truth table for NAND, NOR		
	gates d) Study of half adde of its operation	nterpretation of truth table for Ex-OR, Ex-NOR er using XOR and NAND gates and verification r using XOR and NAND gates and verification		

- 1. M, Morris Mario, "Computer System Architecture", PHI.
- 2. Hearing Jordan, "Computer System Design & Architecture" (A.W.L.)

#### Reference Hooks:

- 3. William Stalling, "Computer Organization & Architecture", Pearson Education Asia.
- 4. V. Carl Hamaeher, "Computer Organization", TMH
- 5. Tannenbaum, 'Structured Computer Organization", PHI.
- 6. Er. Rajiv Chopra, "Computer Architecture", Revised 3rd Edition, S. Chand & Company Pvt. Ltd
  - f) Study of half subtractor and verification of its operation
  - g) Study of full subtractor and verification of its operation
  - h) Realization of logic functions with the help of NAND -Universal Gates
  - i) Realization of logic functions with the help of NOR -Universal Gates
  - j) Verify the truth table of RS flip-flops using NAND and NOR gates
  - k) Verify the truth table of JKflip-flops using NAND and NOR gates
  - 1) Verify the truth table of T and D flip-flops using NAND and NOR gates
  - m) Implementation of 4x1 multiplexer using logic gates
  - n) Implementation of 1x4 demultiplexer using logic gates
  - o) Verify Gray to Binary conversion using NAND gates only
  - p) Verify Gray to Binary conversion using NAND gates only

## PART C: Learning Resources

## Textbooks, Reference Books, Other Resources

## Suggested Readings

#### Textbooks:

- 1.M.Morris Mano, "Computer System Architecture", PHI.
- 2. Heuring Jordan, "Computer System Design & Architecture" (A.W.L.)

#### **Reference Books:**

3. William Stalling, "Computer Organization & Architecture", Pearson Education

Asia.

- 4. V. Carl Hamacher, "Computer Organization", TMH
- 5. Tannenbaum, "Structured Computer Organization", PHI,

Suggestive digital platform web links

Suggested equivalent online courses

https://nptel.ac.in

PART D: Assessment and Evaluation				
Internal Assessment : Continuous Comprehensive Evaluation (CCE) : 40 Marks	External Assessment: University Exam (UE) : 60 Marks			
	Time 03.00 Hours			

Internal Assessment	Marks	<b>External Assessment</b>	Marks	
Hands-on Lab Practice 10 Marks		Practical record file	10 Marks	
Viva	10 Marks	Viva voce practical	10 Marks	
Lab Test from practical list	10 Marks	Table works/ Exercise Assigned (02) in practical exam	30 Marks	
Assignments (Charts/ Model)/ Technology Dissemination/ Excursion/	10 Marks	Reports of excursion/ Lab visits/ Industrial training/ Survey/ Collection/ Models	10 Marks	
Lab visit/ Industrial Training				
Total Excursion/ Lab visits/ Industrial Training is compulsory	40 Marks	Total	60 Marks	



			ART A: Int			
Program: Certificate Class: B.C			C.A.	Year: 1 Year	Session:	
1	Course Code			UOPERCA106		
		Course Title		Operating System Lab		
i	Course Type (Core		Minor			
	Course/Elective/Gene	ric				
	Elective/ Vocational					
•	Pre-Requisite (if any)		Open for All			
•	Course Learning Outcomes		After the completion of this course, a student shall be			
	(CLO)		able to:			
			• 0	perate the Linux system. o administration		
				se Vi Editor		
•	Credit Value		Practical _	2 Cradits		
	Total Marks		Max. Mark		Morks: 16+24	
	Total Warks	рлрт		of the Course	IVIAINS. 10+24	
	No. o			per week): 1 Hr. per week		
	INO. C		otal No. of La			
			/ / /	/ W / -	No. of Labs.	
	Linux:	Sugges	tive List of P	racticals	No. 01 Labs.	
	cd b) Ling rena c) Ling grow d) Ling more od, e) Ling time f) Ling g) Edite time h) Vi e	ux File Commone ux Permission upadd, chmod ux File Conte e, less, grep, c sort, diff. ux Utility Cor e, df, mount, e ux Networki t Crontab file e automatically ditor: Create f ched term wit	nands: touch,  Commands: , groupdel, ch nt & Filter C at, cut, grep, c  mmands: find exit, clear, gzi ng Comman : to wall mess y. file, edit, save hin a file. cut	Commands: head, tail, tac, comm, sed, tee, tr, uniq, we, l, bc, locate, date, cal, sleep, p, gunzip.  Inds: ip, ssh, mail, ping, hos age on system on particular and quit. Highlighting the p, yank, undo.		
				les Other Resources		
Sugge	ested Readings	TCATOOOKS, K	CICICIICE DOO	ks, Other Resources		
Dugge						
	ooks:					
Textb	<b>000ks:</b> Linux by Sumitabha Das	<b>S</b>				

PART D: Assessment and Evaluation						
Internal Assessment : Continuous Comprehensive Evaluation (CCE) : 40 Marks		External Assessment: University Exam (UE): 60 Marks Time 03.00 Hours				
Internal Assessment	Marks	External Assessment	Marks			
Hands-on Lab Practice	10 Marks	Practical record file	10 Marks			
Viva	10 Marks	Viva voce practical	10 Marks			
Lab Test from practical list	10 Marks	Table works/ Exercise Assigned (02) in practical exam	30 Marks			
Assignments (Charts/ Model)/ Technology Dissemination/ Excursion/ Lab visit/ Industrial Training	10 Marks	Reports of excursion/ Lab visits/ Industrial training/ Survey/ Collection/ Models	10 Marks			
Total  Excursion/ Lab visits/ Industrial  Training is compulsory	40 Marks	Total	60 Marks			